

ABSTRACT OF THE DISCLOSURE

In an MOS type solid-state image pickup device in which unit pixels each including a photodiode, a transfer transistor for transferring the signal of the photodiode to a floating node N11, an amplifying transistor for outputting the signal of the floating node to a vertical signal line, and a reset transistor for resetting the floating node are arranged in a matrix form, a P-type MOS transistor is connected between a drainline to which the drain of the reset transistor is connected and a V shift register for selectively supplying the reset voltage to the drain line, and the potential of the floating node is set to the channel voltage of the P-type MOS transistor at the non-selection time.